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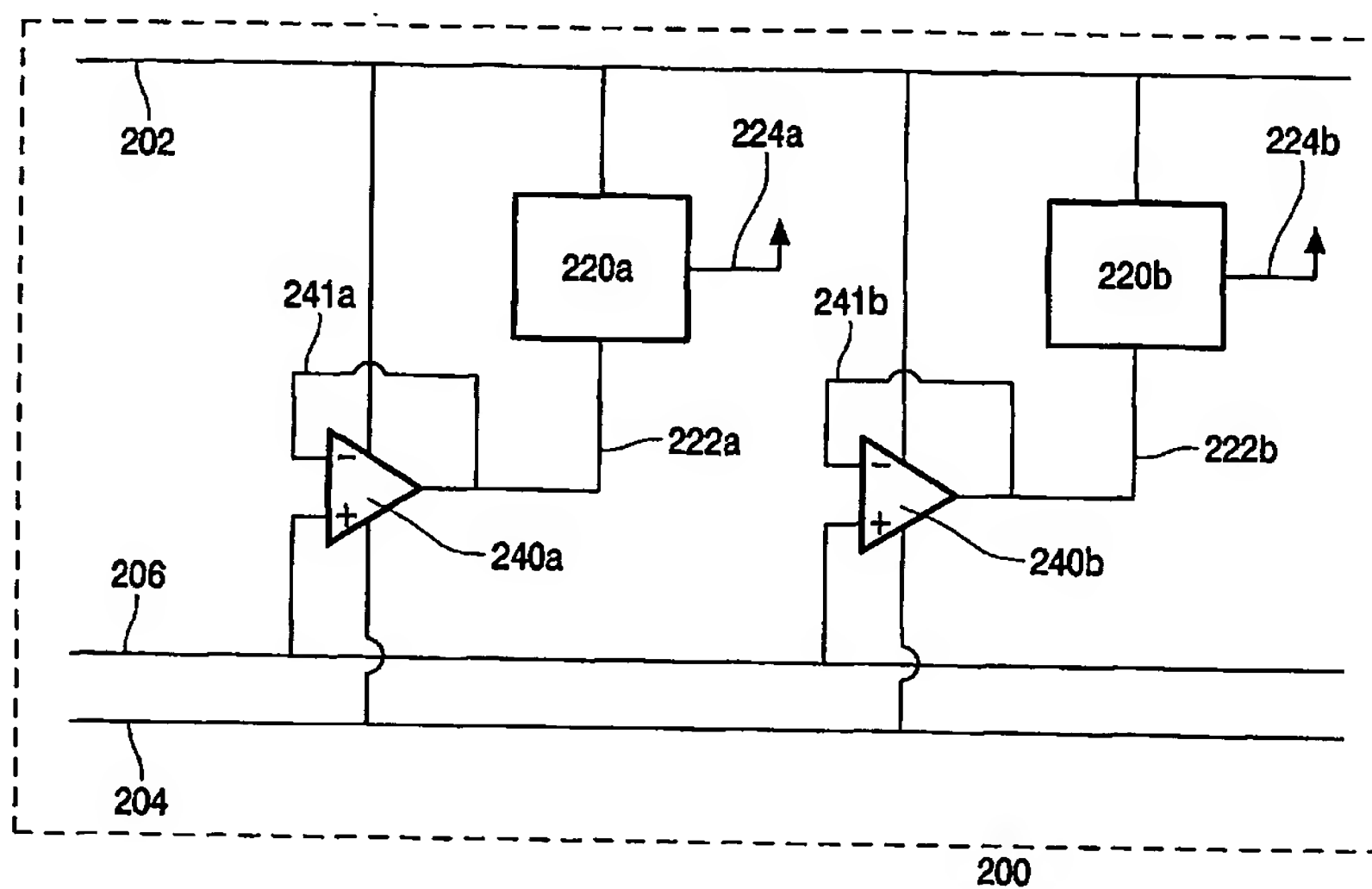
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(54) Title: INTEGRATED CIRCUIT ARRANGEMENT, INTEGRATED CIRCUIT, MATRIX ARRAY OF CIRCUITS AND  
ELECTRONIC DEVICE

(57) Abstract: An IC arrangement (200) has a plurality of IC modules (220a, 220b), the individual IC modules (220a, 220b) being coupled between a first power line (202) and a second power line (204) via a voltage generator (240a, 240b). The voltage generators (240a, 240b) are powered via the first power line (202) and the second power line (204) and are arranged to regenerate a reference voltage on a reference power line (106) for providing the IC modules (220a, 220b) with the regenerated voltage on respective internal power lines (222a, 222b). A feedback loop (242a, 242b) from the internal power lines (222a, 222b) to the voltage generator (240a, 240b) ensures that the voltage on the internal power lines (222a, 222b) remains substantially constant, even if substantial current fluctuations on the first power line (202), the second power line (204) or the internal power line (222a, 222b) occur. The IC arrangement (200) is particularly suitable as a driver circuit for a matrix array device.

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— *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments*

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# INTERNATIONAL SEARCH REPORT

International Application No  
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**A. CLASSIFICATION OF SUBJECT MATTER**  
IPC 7 G09G3/20 G09G3/32 G09G3/36

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)  
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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

WPI Data, EPO-Internal, PAJ

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2001/015709 A1 (IZAWA TETSURO ET AL) 23 August 2001 (2001-08-23) paragraphs '0129!-'0147!; figure 6 ---	1-10
X	US 5 576 737 A (ISOZAKI SHINGO) 19 November 1996 (1996-11-19) column 13, line 46 -column 14, line 33; figures 8,9 -----	1,2,5,6, 8-10

☐ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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Information on patent family members

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